

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application.

LISTING OF CLAIMS

Please amend the Claims as follows:

Claims 1-22 (cancelled)

23. (currently amended) An apparatus, comprising:

an integrated circuit comprising state machine circuitry capable of generating one or more signals to operate a CD-ROM drive and play audio CDs, said integrated circuit further comprising circuitry capable of ~~selectively coupling or~~ isolating a digital computer bus ~~and from~~ said CD-ROM drive.

24. (currently amended) The apparatus of claim 23, wherein:

said integrated circuit comprising transmission gates capable of ~~selectively coupling or~~ isolating said digital computer bus ~~and from~~ said CD-ROM drive.

25. (previously presented) The apparatus of claim 23, further comprising:

host-bus interface circuitry capable of exchanging bus commands and data with said CD-ROM drive.

26. (currently amended) A system, comprising:

a computer system comprising a system CPU, a computer bus and a CD-ROM drive; and
an integrated circuit comprising state machine circuitry capable of generating one or more signals to operating a CD-ROM drive and play audio CDs, said integrated circuit further

comprising circuitry capable of ~~selectively coupling or~~ isolating a digital computer bus ~~and~~ from said CD-ROM drive.

27. (currently amended) The system of claim 26, wherein:

said integrated circuit comprising transmission gates capable of ~~selectively coupling or~~ isolating a digital computer bus ~~and~~ from said CD-ROM drive.

28. (previously presented) The system of claim 26, further comprising:

host-bus interface circuitry capable of exchanging bus commands and data with said CD-ROM drive.

29. (previously presented) The system of claim 26, wherein:

said computer bus comprises a PCI bus.

30. (original) The system of claim 26, further comprising:

one or more buttons electrically coupled to said integrated circuit, said buttons capable of controlling at least one operation performed by said integrated circuit.

31. (new) The apparatus of claim 23, wherein the digital computer bus is an IDE bus.